

High performance type

Performance specifications

Item	XBC/XEC-DR32H	XBC/XEC-DN32H	XBC/XEC-DR64H	XBC/XEC-DN64H
	XBC-DR32H/DC ^{*1)} XEC-DR32H/DI	XEC-DP32H ^{*1)} XBC-DN32H/DC	XBC-DR64H/DC ^{*1)} XEC-DR64H/DI	XEC-DP64H ^{*1)} XBC-DN64H/DC
Control method	Repetitive, cyclic, interrupt, constant scan			
I/O control method	Refresh mode (Batch processing by scan synchronization), Direct mode by instruction			
Programming language	Ladder diagram or IEC standard (LD, SFC, ST) ^{*1)}			
Processing speed	83 ns / Step			
Program capacity	15Kstep (IEC type: 200KB)			
Main unit I/O points	32 (Input:16, Output:16)	32 (Input:16, Output:16)	64 (Input: 32, Output: 32)	64 (Input: 32, Output: 32)
Max. I/O points (Main + Expansion 10 stages)	352 points		384 points	
Total program	128			
Operation mode	RUN, STOP, DEBUG			
Self diagnosis	Detects errors of scan time, memory error, I/O error, battery error, power error, etc.			
Program port	USB (Rev 1.1), RS-232C 1 channel (Loader)			
Retain data at power failure	Latch area setting at basic parameter			
Built-in functions	RS-232C / RS-485 (2 ch), Pulse catch, Input filter, External interrupt, PID control, High-speed counter, Positioning, RTC			
Data memory				
XBC		XEC (IEC type)		
P	P0000 ~ P1023F (16,384 points)	Symbolic variable	A	32KB (Max. 16KB retain setting available)
M	M0000 ~ M1023F (16,384 points)	Input variable	I	2KB (%IX 15.15.63)
K	K0000 ~ K4095F (65,536 points)	Output variable	Q	2KB (%QX 15.15.63)
L	L0000 ~ L2047F (32,768 points)	Direct variable	M	16KB (Max. 8KB retain setting available)
F	F0000 ~ F1023F (16,384 points)		R	20KB (1 block)
T	100ms, 10ms, 1ms: T0000 ~ T1023 (1,024)(Adjustable by parameter setting)		W	20KB
C	C0000 ~ C1023 (1,024)	Flag variable	F	2KB
S	S00.00 ~ S127.99		K	8KB
D	D0000 ~ D10239 (10,240 word)		L	4KB
U	U00.00 ~ U0A.31 (Analog data refresh area: 352 word)		N	10KB
Z	Z000 ~ Z127 (128 word)		U	1KB
N	N000 ~ N5119 (5,120 word)	Flash area	R	20KB (2 blocks)

^{*1)} XEC is IEC standard language programming.