## SIEMENS

## Data sheet

## 6ES7412-2XK07-0AB0



SIMATIC S7-400, CPU 412-2 Central processing unit with: Work memory 1 MB, (0.5 MB code; 0.5 MB data) 1st interface MPI/DP 12 Mbit/s, 2nd interface PROFIBUS DP,

General information	
Product type designation	CPU 412-2
HW functional status	01
Firmware version	V7.0
Product function	
Isochronous mode	Yes; For PROFIBUS only
Engineering with	
<ul> <li>Programming package</li> </ul>	STEP 7 V5.4 or higher with HSP 261
CiR - Configuration in RUN	
CiR synchronization time, basic load	100 ms
CiR synchronization time, time per I/O byte	30 µs
Supply voltage	
Rated value (DC)	Power supply via system power supply
Input current	
from backplane bus 5 V DC, typ.	0.9 A
from backplane bus 5 V DC, max.	1.1 A
from backplane bus 24 V DC, max.	300 mA; 150 mA per DP interface
from interface 5 V DC, max.	90 mA; At each DP interface
Power loss	
Power loss, typ.	4.5 W
Power loss, max.	5.5 W
Memory	
Type of memory	RAM
Work memory	
integrated	1 Mbyte
<ul> <li>integrated (for program)</li> </ul>	512 kbyte
<ul> <li>integrated (for data)</li> </ul>	512 kbyte
expandable	No
Load memory	
expandable FEPROM	Yes; with Memory Card (FLASH)
<ul> <li>expandable FEPROM, max.</li> </ul>	64 Mbyte
<ul> <li>integrated RAM, max.</li> </ul>	512 kbyte
expandable RAM	Yes; with Memory Card (RAM)
<ul> <li>expandable RAM, max.</li> </ul>	64 Mbyte
Backup	
present	Yes
with battery	Yes; all data
without battery	No
Battery	
Backup battery	

Backup current, typ.	180 μA; up to 40 °C
Backup current, max.	850 µA
Backup time, max.	Dealt with in the module data manual with the secondary conditions and the factors of influence
<ul> <li>Feeding of external backup voltage to CPU</li> </ul>	5 V DC to 15 V DC
PU processing times	
for bit operations, typ.	31.25 ns
for word operations, typ.	31.25 ns
for fixed point arithmetic, typ.	31.25 ns
for floating point arithmetic, typ.	62.5 ns
PU-blocks	
DB	
• Number, max.	3 000; Number range: 1 to 16000
• Size, max.	64 kbyte
FB	
• Number, max.	1 500; Number range: 0 to 7999
• Size, max.	64 kbyte
FC	
• Number, max.	1 500; Number range: 0 to 7999
• Size, max.	64 kbyte
OB	
• Number, max.	see instruction list
• Size, max.	64 kbyte
Number of free cycle OBs	1; OB 1
Number of time alarm OBs	2; OB 10, 11
<ul> <li>Number of delay alarm OBs</li> </ul>	2; OB 20, 21
<ul> <li>Number of cyclic interrupt OBs</li> </ul>	2; OB 32, 35 (shortest cycle that can be set = 500 $\mu$ s)
Number of process alarm OBs	2; OB 40, 41
Number of DPV1 alarm OBs	3; OB 55-57
<ul> <li>Number of isochronous mode OBs</li> </ul>	2; OB 61-62
<ul> <li>Number of multicomputing OBs</li> </ul>	1; OB 60
Number of background OBs	1; OB 90
Number of startup OBs	3: OB 100-102
<ul> <li>Number of asynchronous error OBs</li> </ul>	9; OB 80-88
Number of synchronous error OBs	2; OB 121, 122
Nesting depth	
per priority class	24
additional within an error OB	1
ounters, timers and their retentivity	
S7 counter	
Number	2 048
Retentivity	
-	Yes
— adjustable	Yes 0
— adjustable — lower limit	0
— adjustable — lower limit — upper limit	0 2 047
<ul> <li>adjustable</li> <li>lower limit</li> <li>upper limit</li> <li>preset</li> </ul>	0
<ul> <li>adjustable</li> <li>lower limit</li> <li>upper limit</li> <li>preset</li> <li>Counting range</li> </ul>	0 2 047 Z 0 to Z 7
<ul> <li>adjustable</li> <li>lower limit</li> <li>upper limit</li> <li>preset</li> <li>Counting range</li> <li>lower limit</li> </ul>	0 2 047 Z 0 to Z 7 0
<ul> <li>adjustable</li> <li>lower limit</li> <li>upper limit</li> <li>preset</li> <li>Counting range</li> <li>lower limit</li> <li>upper limit</li> </ul>	0 2 047 Z 0 to Z 7
<ul> <li>adjustable</li> <li>lower limit</li> <li>upper limit</li> <li>preset</li> <li>Counting range</li> <li>lower limit</li> <li>upper limit</li> <li>IEC counter</li> </ul>	0 2 047 Z 0 to Z 7 0 999
<ul> <li>adjustable</li> <li>lower limit</li> <li>upper limit</li> <li>preset</li> <li>Counting range</li> <li>lower limit</li> <li>upper limit</li> <li>IEC counter</li> <li>present</li> </ul>	0 2 047 Z 0 to Z 7 0 999 Yes
<ul> <li>adjustable</li> <li>lower limit</li> <li>upper limit</li> <li>preset</li> </ul> Counting range <ul> <li>lower limit</li> <li>upper limit</li> </ul> IEC counter <ul> <li>present</li> <li>Type</li> </ul>	0 2 047 Z 0 to Z 7 0 999 Ves SFB
<ul> <li>adjustable</li> <li>lower limit</li> <li>upper limit</li> <li>preset</li> </ul> Counting range <ul> <li>lower limit</li> <li>upper limit</li> </ul> IEC counter <ul> <li>present</li> <li>Type</li> <li>Number</li> </ul>	0 2 047 Z 0 to Z 7 0 999 Yes
<ul> <li>adjustable</li> <li>lower limit</li> <li>upper limit</li> <li>preset</li> </ul> Counting range <ul> <li>lower limit</li> <li>upper limit</li> </ul> IEC counter <ul> <li>present</li> <li>Type</li> <li>Number</li> </ul> S7 times	0 2 047 Z 0 to Z 7 0 999 Yes SFB Unlimited (limited only by RAM capacity)
<ul> <li>adjustable</li> <li>lower limit</li> <li>preset</li> <li>Counting range</li> <li>lower limit</li> <li>upper limit</li> <li>IEC counter</li> <li>present</li> <li>Type</li> <li>Number</li> <li>S7 times</li> <li>Number</li> </ul>	0 2 047 Z 0 to Z 7 0 999 Ves SFB
<ul> <li>adjustable</li> <li>lower limit</li> <li>upper limit</li> <li>preset</li> </ul> Counting range <ul> <li>lower limit</li> <li>upper limit</li> </ul> IEC counter <ul> <li>present</li> <li>Type</li> <li>Number</li> </ul> S7 times <ul> <li>Number</li> <li>Retentivity</li> </ul>	0 2 047 Z 0 to Z 7 0 999 Yes SFB Unlimited (limited only by RAM capacity) 2 048
<ul> <li>adjustable</li> <li>lower limit</li> <li>upper limit</li> <li>preset</li> <li>Counting range</li> <li>lower limit</li> <li>upper limit</li> <li>IEC counter</li> <li>present</li> <li>Type</li> <li>Number</li> <li>S7 times</li> <li>Number</li> <li>Retentivity</li> <li>adjustable</li> </ul>	0 2 047 Z 0 to Z 7 0 999 999 Ves SFB Unlimited (limited only by RAM capacity) v
<ul> <li>adjustable</li> <li>lower limit</li> <li>upper limit</li> <li>preset</li> <li>Counting range</li> <li>lower limit</li> <li>upper limit</li> <li>IEC counter</li> <li>present</li> <li>Type</li> <li>Number</li> <li>S7 times</li> <li>Number</li> <li>Retentivity</li> <li>adjustable</li> <li>lower limit</li> </ul>	0 2 047 2 0 to Z 7 0 999 Ves SFB Unlimited (limited only by RAM capacity) Ves 2 048 Ves 0
<ul> <li>adjustable</li> <li>lower limit</li> <li>upper limit</li> <li>preset</li> <li>Counting range</li> <li>lower limit</li> <li>upper limit</li> <li>IEC counter</li> <li>present</li> <li>Type</li> <li>Number</li> <li>S7 times</li> <li>Number</li> <li>Retentivity</li> <li>adjustable</li> </ul>	0 2 047 Z 0 to Z 7 0 999 999 Ves SFB Unlimited (limited only by RAM capacity) v

— lower limit	10 ms
— upper limit	9 990 s
IEC timer	
• present	Yes
• Type	SFB
Number	Unlimited (limited only by RAM capacity)
Data areas and their retentivity	
Retentive data area (incl. timers, counters, flags), max.	Total working and load memory (with backup battery)
Flag	rotal working and rota memory (with backup battery)
• Size, max.	4 kbyte; Size of bit memory address area
Retentivity available	Yes
Retentivity preset	MB 0 to MB 15
Number of clock memories	8; in 1 memory byte
Local data	
• adjustable, max.	8 kbyte
• preset	4 kbyte
Address area	
I/O address area	
Inputs	4 kbyte
Outputs	4 kbyte
Process image	
Inputs, adjustable	4 kbyte
• Outputs, adjustable	4 kbyte
Inputs, default	128 byte
Outputs, default	128 byte
• consistent data, max.	244 byte
<ul> <li>Access to consistent data in process image</li> </ul>	Yes
Subprocess images	
<ul> <li>Number of subprocess images, max.</li> </ul>	15
Digital channels	
Inputs	32 768
— of which central	32 768
Outputs	32 768
— of which central	32 768
Analog channels	
Inputs	2 048
— of which central	2 048
Outputs	2 048
— of which central	2 048
Hardware configuration	
Number of expansion units, max.	21
connectable OPs	47
Multicomputing	Yes; 4 CPUs max. (with UR1 or UR2)
Interface modules	
<ul> <li>Number of connectable IMs (total), max.</li> </ul>	6
Number of connectable IM 460s, max.	6
Number of connectable IM 463s, max.	4; IM 463-2
Number of DP masters	
integrated	2 10: CD 442 5 Evtended
• via CP	10; CP 443-5 Extended
• via IM 467	4
Mixed mode IM + CP permitted	No; IM 467 cannot be used jointly with CP 443-5 Ext. or CP 443-1 in PROFINET IO mode
via interface module	0
Number of pluggable S5 modules (via adapter capsule in central device), max.	6
Number of IO Controllers	
integrated	0
• via CP	4; Max. 4 in the central controller; no mixed operation of different CP 443-1 types in PROFINET IO mode
Number of operable FMs and CPs (recommended)	

• FM	Limited by number of slots and number of connections
• CP, PtP	CP 440: Limited by number of slots; CP 441: Limited by number of slots and number of connections
PROFIBUS and Ethernet CPs	14; In total max. 10 CPs as DP master and PROFINET controller, of which up to 10 IMs or CPs as DP master and up to 4 CPs as PROFINET controller

	to 10 IMs or CPs as DP master and up to 4 CPs as PROFINET controller
Slots	
required slots	1
Time of day	
Clock	
<ul> <li>Hardware clock (real-time)</li> </ul>	Yes
<ul> <li>retentive and synchronizable</li> </ul>	Yes
Resolution	1 ms
<ul> <li>Deviation per day (buffered), max.</li> </ul>	1.7 s; Power off
<ul> <li>Deviation per day (unbuffered), max.</li> </ul>	8.6 s; For power On
Operating hours counter	
Number	16
Number/Number range	0 to 15
<ul> <li>Range of values</li> </ul>	SFCs 2, 3 and 4: 0 to 32767 hours SFC 101: 0 to 2^31 - 1 hours
Granularity	1 h
retentive	Yes
Clock synchronization	
<ul> <li>supported</li> </ul>	Yes
• to MPI, master	Yes
● to MPI, slave	Yes
• to DP, master	Yes
• to DP, slave	Yes
• in AS, master	Yes
• in AS, slave	Yes
<ul> <li>on Ethernet via NTP</li> </ul>	No; Via CP
• to IF 964 DP	No
Time difference in system when synchronizing via	
• MPI, max.	200 ms
Interfaces	
Interfaces/bus type	1 x MPI/PROFIBUS DP, 1 x PROFIBUS DP
Number of RS 485 interfaces	2; Combined MPI / PROFIBUS DP and PROFIBUS DP
1. Interface	
Interface type	MPI/PROFIBUS DP
Isolated	Yes
Interface types	
• RS 485	Yes
Output current of the interface, max.	150 mA
Protocols	
• MPI	Yes
PROFIBUS DP master	Yes
PROFIBUS DP slave	Yes
MPI	
Number of connections	32; If a diagnostics repeater is used on the line, the number of connection resources on the line is reduced by 1
Transmission rate, max.	12 Mbit/s
Services	
— PG/OP communication	Yes
— Routing	Yes
— Global data communication	Yes
— S7 basic communication	Yes
— S7 communication	Yes
— S7 communication, as client	Yes
— S7 communication, as server	Yes
PROFIBUS DP master	
<ul> <li>PROFIBUS DP master</li> <li>Number of connections, max.</li> </ul>	16; If a diagnostics repeater is used on the line, the number of connection resources on the line is reduced by 1

Services	
— PG/OP communication	Yes
— Routing	Yes; S7 routing
<ul> <li>— Global data communication</li> </ul>	No
<ul> <li>— S7 basic communication</li> </ul>	Yes
— S7 communication	Yes
- S7 communication, as client	Yes
- S7 communication, as server	Yes
— Equidistance	Yes
— Isochronous mode	Yes
- SYNC/FREEZE	Yes
<ul> <li>Activation/deactivation of DP slaves</li> </ul>	Yes
<ul> <li>Direct data exchange (slave-to-slave</li> </ul>	Yes
communication)	
— DPV1	Yes
Address area	
— Inputs, max.	2 kbyte
— Outputs, max.	2 kbyte
User data per DP slave	
— User data per DP slave, max.	244 byte
— Inputs, max.	244 byte
— Outputs, max.	244 byte
— Slots, max.	244
— per slot, max.	128 byte
PROFIBUS DP slave	
Number of connections	16
GSD file	http://support.automation.siemens.com/WW/view/en/113652
	12 Mbit/s
Transmission rate, max.	
automatic baud rate search	No
Address area, max.	32; Virtual slots
User data per address area, max.	32 byte
— of which consistent, max.	32 byte
Services	
— PG/OP communication	Yes; with interface active
— Routing	Yes; with interface active
<ul> <li>Global data communication</li> </ul>	No
<ul> <li>— S7 basic communication</li> </ul>	No
— S7 communication	Yes
<ul> <li>— S7 communication, as client</li> </ul>	Yes
- S7 communication, as server	Yes
— Direct data exchange (slave-to-slave	No
communication)	
— DPV1	No
Transfer memory	
— Inputs	244 byte
— Outputs	244 byte
2. Interface	
Interface type	PROFIBUS DP
Isolated	Yes
Number of connection resources	16
Interface types	
• RS 485	Yes
Output current of the interface, max.	150 mA
Protocols	
PROFIBUS DP master	Yes
PROFIBUS DP slave	Yes
PROFIBUS DP master	10
Number of connections, max.	16
• Transmission rate, max.	12 Mbit/s
Number of DP slaves, max.	64
Services	

— PG/OP communication	Yes
- Routing	Yes; S7 routing
— Global data communication	No
— S7 basic communication	Yes
— S7 communication	Yes
— S7 communication, as client	Yes
— S7 communication, as server	Yes
— Equidistance	Yes
— Isochronous mode	Yes
- SYNC/FREEZE	Yes
- Activation/deactivation of DP slaves	Yes
<ul> <li>— Direct data exchange (slave-to-slave communication)</li> </ul>	Yes
— DPV1	Yes
Address area	
— Inputs, max.	4 kbyte
— Outputs, max.	4 kbyte
User data per DP slave	
— User data per DP slave, max.	244 byte
— Inputs, max.	244 byte
— Outputs, max.	244 byte
— Slots, max.	244
— per slot, max.	128 byte
PROFIBUS DP slave	
Number of connections	16
GSD file	http://support.automation.siemens.com/WW/view/en/113652
• Transmission rate, max.	12 Mbit/s
Address area, max.	32
• User data per address area, max.	32 byte
— of which consistent, max.	32 byte
Services	
- Routing	Yes; with interface active
Transfer memory	
— Inputs	244 byte
— Outputs	244 byte
Protocols	
SIMATIC communication	
S7 routing	Yes
Open IE communication	
<ul> <li>ISO-on-TCP (RFC1006)</li> </ul>	Via CP 443-1 and loadable FB
— Data length, max.	1 452 bytes via CP 443-1 Adv.
Web server	
supported	No
Isochronous mode	
Equidistance	Yes
4	
Number of DP masters with isochronous mode	2
· · · · · · · · · · · · · · · · · · ·	2 244 byte
Number of DP masters with isochronous mode	
Number of DP masters with isochronous mode User data per isochronous slave, max.	244 byte
Number of DP masters with isochronous mode User data per isochronous slave, max. shortest clock pulse	244 byte 1.5 ms; 0.5 ms without use of SFC 126, 127
Number of DP masters with isochronous mode User data per isochronous slave, max. shortest clock pulse max. cycle	244 byte 1.5 ms; 0.5 ms without use of SFC 126, 127
Number of DP masters with isochronous mode         User data per isochronous slave, max.         shortest clock pulse         max. cycle         communication functions / header	244 byte 1.5 ms; 0.5 ms without use of SFC 126, 127 32 ms
Number of DP masters with isochronous mode         User data per isochronous slave, max.         shortest clock pulse         max. cycle         communication functions / header         PG/OP communication	244 byte 1.5 ms; 0.5 ms without use of SFC 126, 127 32 ms Yes
Number of DP masters with isochronous mode         User data per isochronous slave, max.         shortest clock pulse         max. cycle         communication functions / header         PG/OP communication         • Number of connectable OPs without message processing	244 byte 1.5 ms; 0.5 ms without use of SFC 126, 127 32 ms Yes 47
Number of DP masters with isochronous mode         User data per isochronous slave, max.         shortest clock pulse         max. cycle         communication functions / header         PG/OP communication         • Number of connectable OPs without message processing         • Number of connectable OPs with message processing	244 byte 1.5 ms; 0.5 ms without use of SFC 126, 127 32 ms Yes 47 47; When using Alarm_S/SQ and Alarm_D/DQ
Number of DP masters with isochronous mode         User data per isochronous slave, max.         shortest clock pulse         max. cycle         communication functions / header         PG/OP communication         • Number of connectable OPs without message processing         • Number of connectable OPs with message processing         • Number of connectable OPs with message processing	244 byte 1.5 ms; 0.5 ms without use of SFC 126, 127 32 ms Yes 47 47; When using Alarm_S/SQ and Alarm_D/DQ
Number of DP masters with isochronous mode         User data per isochronous slave, max.         shortest clock pulse         max. cycle         communication functions / header         PG/OP communication         • Number of connectable OPs without message processing         • Number of connectable OPs with message processing         • Number of connectable OPs with message processing         • Option	244 byte 1.5 ms; 0.5 ms without use of SFC 126, 127 32 ms Yes 47 47; When using Alarm_S/SQ and Alarm_D/DQ Yes
Number of DP masters with isochronous mode         User data per isochronous slave, max.         shortest clock pulse         max. cycle         communication functions / header         PG/OP communication         • Number of connectable OPs without message processing         • Number of connectable OPs with message processing         Data record routing         Global data communication         • supported	244 byte 1.5 ms; 0.5 ms without use of SFC 126, 127 32 ms Yes 47 47; When using Alarm_S/SQ and Alarm_D/DQ Yes Yes
Number of DP masters with isochronous mode         User data per isochronous slave, max.         shortest clock pulse         max. cycle         communication functions / header         PG/OP communication         • Number of connectable OPs without message processing         • Number of connectable OPs with message processing         Data record routing         Global data communication         • supported         • Number of GD loops, max.	244 byte 1.5 ms; 0.5 ms without use of SFC 126, 127 32 ms Yes 47 47; When using Alarm_S/SQ and Alarm_D/DQ Yes Yes 8
Number of DP masters with isochronous mode         User data per isochronous slave, max.         shortest clock pulse         max. cycle         communication functions / header         PG/OP communication         • Number of connectable OPs without message processing         • Number of connectable OPs with message processing         Data record routing         Global data communication         • supported         • Number of GD loops, max.         • Number of GD packets, transmitter, max.	244 byte 1.5 ms; 0.5 ms without use of SFC 126, 127 32 ms Yes 47 47; When using Alarm_S/SQ and Alarm_D/DQ Yes Yes 8 8
Number of DP masters with isochronous mode         User data per isochronous slave, max.         shortest clock pulse         max. cycle         communication functions / header         PG/OP communication         • Number of connectable OPs without message processing         • Number of connectable OPs with message processing         Data record routing         Global data communication         • supported         • Number of GD loops, max.         • Number of GD packets, transmitter, max.         • Number of GD packets, receiver, max.	244 byte 1.5 ms; 0.5 ms without use of SFC 126, 127 32 ms Yes 47 47; When using Alarm_S/SQ and Alarm_D/DQ Yes Yes 8 8 8 16

S7 basic communication	
communication     or s7 basic communication	Yes
User data per job, max.	76 byte
User data per job (of which consistent), max.	1 variable
S7 communication	
• supported	Yes
• as server	Yes
as client	Yes
• User data per job, max.	64 kbyte
<ul> <li>User data per job (of which consistent), max.</li> </ul>	462 byte; 1 variable
S5 compatible communication	
<ul> <li>supported</li> </ul>	Yes; Via FC AG_SEND and AG_RECV, max. via 10 CP 443-1 or 443-5
<ul> <li>User data per job, max.</li> </ul>	8 kbyte
<ul> <li>User data per job (of which consistent), max.</li> </ul>	240 byte
Number of simultaneous AG-SEND/AG-RECV orders per	24/24
CPU, max.	
Standard communication (FMS)	Vee: Vie CD and leadable ED
supported	Yes; Via CP and loadable FB
Number of connections  • overall	48
usable for PG communication	48
usable for PG communication     — reserved for PG communication	47
- adjustable for PG communication, max.	0
usable for OP communication	47
— reserved for OP communication	1
— adjustable for OP communication, max.	0
usable for S7 basic communication	46
- reserved for S7 basic communication	0
- adjustable for S7 basic communication, max.	0
usable for S7 communication	46
<ul> <li>reserved for S7 communication</li> </ul>	0
— adjustable for S7 communication, max.	0
usable for routing	23
— reserved for routing	0
— adjustable for routing, max.	0
S7 message functions	
Number of login stations for message functions, max.	47; Max. 47 with Alarm_S/SQ and Alarm_D/DQ (OPs); max. 8 with Alarm, Alarm_8, Alarm_8P, Notify and Notify_8 (e.g. WinCC)
Symbol-related messages	Yes
SCAN procedure	Yes
Program alarms	Yes
Process diagnostic messages	Yes
simultaneously active Alarm-S blocks, max.	250; Simultaneously active alarm_S/SQ blocks or alarm_D/DQ blocks
Alarm 8-blocks	Yes
<ul> <li>Number of instances for alarm 8 and S7 communication blocks, max.</li> </ul>	300
preset, max.	150
Process control messages Number of archives that can log on simultaneously (SFB 37	Yes 4
AR_SEND)	
Number of messages • overall, max.	
• in 100 ms grid, max.	256
-	256 0
	0
<ul><li>in 500 ms grid, max.</li><li>in 1000 ms grid, max.</li></ul>	
in 1000 ms grid, max.     in 1000 ms grid, max. Number of additional values	0 256
• in 1000 ms grid, max.	0 256
• in 1000 ms grid, max. Number of additional values	0 256 256
<ul> <li>in 1000 ms grid, max.</li> <li>Number of additional values</li> <li>with 100 ms grid, max.</li> </ul>	0 256 256 0
<ul> <li>in 1000 ms grid, max.</li> <li>Number of additional values</li> <li>with 100 ms grid, max.</li> <li>with 500, 1000 ms grid, max.</li> </ul>	0 256 256 0
<ul> <li>in 1000 ms grid, max.</li> <li>Number of additional values</li> <li>with 100 ms grid, max.</li> <li>with 500, 1000 ms grid, max.</li> <li>Test commissioning functions</li> </ul>	0 256 256 1

Status/control	
Status/control variable	Yes; Up to 16 variable tables
Variables	Inputs/outputs, memory bits, DBs, distributed I/Os, timers, counters
Number of variables, max.	70; Status/control
Forcing	
• Forcing	Yes
<ul> <li>Forcing, variables</li> </ul>	Inputs, outputs, bit memories, peripheral inputs, peripheral outputs
Number of variables, max.	64
Diagnostic buffer	
• present	Yes
<ul> <li>Number of entries, max.</li> </ul>	3 200
— adjustable	Yes
— preset	120
Service data	
<ul> <li>can be read out</li> </ul>	Yes
Standards, approvals, certificates	
CE mark	Yes
CSA approval	Yes
UL approval	Yes
cULus	Yes
FM approval	Yes
RCM (formerly C-TICK)	Yes
KC approval	Yes
EAC (formerly Gost-R)	Yes
Use in hazardous areas	
• ATEX	ATEX II 3G Ex nA IIC T4 Gc
Ambient conditions	
Ambient temperature during operation	
• min.	0°0
	0 ℃
max. configuration / header	00 C
Configuration software	Ver
• STEP 7	Yes
configuration / programming / header	
<ul> <li>Command set</li> </ul>	
No stine la colo	see instruction list
Nesting levels	7
Access to consistent data in process image	7 Yes
<ul><li>Access to consistent data in process image</li><li>System functions (SFC)</li></ul>	7 Yes see instruction list
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> </ul>	7 Yes
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language</li> </ul>	7 Yes see instruction list see instruction list
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> </ul> </li> </ul>	7 Yes see instruction list see instruction list Yes
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> </ul> </li> </ul>	7 Yes see instruction list see instruction list Yes Yes
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> </ul> </li> </ul>	7 Yes see instruction list see instruction list Yes
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> <li>SCL</li> </ul> </li> </ul>	7 Yes see instruction list see instruction list Yes Yes
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> </ul> </li> </ul>	7 Yes see instruction list see instruction list Yes Yes Yes
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> <li>SCL</li> </ul> </li> </ul>	7 Yes see instruction list see instruction list Yes Yes Yes Yes
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> <li>SCL</li> <li>CFC</li> </ul> </li> </ul>	7 Yes see instruction list see instruction list Yes Yes Yes Yes Yes Yes
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> <li>SCL</li> <li>CFC</li> <li>GRAPH</li> <li>HiGraph®</li> </ul> </li> </ul>	7 Yes see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> <li>SCL</li> <li>CFC</li> <li>GRAPH</li> <li>HiGraph®</li> </ul> </li> </ul>	7 Yes see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> <li>SCL</li> <li>CFC</li> <li>GRAPH</li> <li>HiGraph®</li> </ul> </li> </ul>	7 Yes see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes Yes Yes
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> <li>SCL</li> <li>CFC</li> <li>GRAPH</li> <li>HiGraph®</li> </ul> </li> <li>configuration / programming / number of simultaneously</li> <li>DPSYC_FR</li> </ul>	7 Yes see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes Yes Yes
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> <li>SCL</li> <li>CFC</li> <li>GRAPH</li> <li>HiGraph®</li> </ul> </li> <li>configuration / programming / number of simultaneously</li> <li>DPSYC_FR</li> <li>D_ACT_DP</li> </ul>	7 Yes see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes Yes Yes
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> <li>SCL</li> <li>CFC</li> <li>GRAPH</li> <li>HiGraph®</li> </ul> </li> <li>configuration / programming / number of simultaneously</li> <li>DPSYC_FR</li> <li>D_ACT_DP</li> <li>RD_REC</li> </ul>	7 Yes see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes Yes SFC / header 2; SFC 11; per interface 8; SFC 12; per interface 8; SFC 59; per interface
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> <li>SCL</li> <li>CFC</li> <li>GRAPH</li> <li>HiGraph®</li> </ul> </li> <li>configuration / programming / number of simultaneously</li> <li>DPSYC_FR</li> <li>D_ACT_DP</li> <li>RD_REC</li> <li>WR_REC</li> <li>WR_PARM</li> </ul>	7 Yes see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes SFC / header 2; SFC 11; per interface 8; SFC 12; per interface 8; SFC 59; per interface 8; SFC 59; per interface
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> <li>SCL</li> <li>CFC</li> <li>GRAPH</li> <li>HiGraph®</li> </ul> </li> <li>configuration / programming / number of simultaneously</li> <li>DPSYC_FR</li> <li>D_ACT_DP</li> <li>RD_REC</li> <li>WR_REC</li> </ul>	7 Yes see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes Yes SFC / header 2; SFC 11; per interface 8; SFC 59; per interface 8; SFC 59; per interface 8; SFC 59; per interface 8; SFC 55; per interface 8; SFC 55; per interface
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> <li>SCL</li> <li>CFC</li> <li>GRAPH</li> <li>HiGraph®</li> </ul> </li> <li>configuration / programming / number of simultaneously</li> <li>DPSYC_FR</li> <li>D_ACT_DP</li> <li>RD_REC</li> <li>WR_PARM</li> <li>PARM_MOD</li> <li>WR_DPARM</li> </ul>	7 Yes see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes Yes Yes
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> <li>SCL</li> <li>CFC</li> <li>GRAPH</li> <li>HiGraph®</li> </ul> </li> <li>configuration / programming / number of simultaneously</li> <li>DPSYC_FR</li> <li>D_ACT_DP</li> <li>RD_REC</li> <li>WR_REC</li> <li>WR_PARM</li> <li>PARM_MOD</li> <li>WR_DPARM</li> <li>DPNRM_DG</li> </ul>	7 Yes see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes Yes Yes
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> <li>SCL</li> <li>CFC</li> <li>GRAPH</li> <li>HiGraph®</li> </ul> </li> <li>configuration / programming / number of simultaneously</li> <li>DPSYC_FR</li> <li>D_ACT_DP</li> <li>RD_REC</li> <li>WR_PARM</li> <li>PARM_MOD</li> <li>WR_DPARM</li> <li>DPNRM_DG</li> <li>RDSYSST</li> </ul>	7 Yes see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes Yes Store SFC / header 2; SFC 11; per interface 8; SFC 12; per interface 8; SFC 59; per interface 8; SFC 59; per interface 8; SFC 56; per interface 8; SFC 57; per interface 8; SFC 51; per interface 8; SFC 51; per interface
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> <li>SCL</li> <li>CFC</li> <li>GRAPH</li> <li>HiGraph®</li> </ul> </li> <li>configuration / programming / number of simultaneously</li> <li>DPSYC_FR</li> <li>D_ACT_DP</li> <li>RD_REC</li> <li>WR_REC</li> <li>WR_PARM</li> <li>PARM_MOD</li> <li>WR_DPARM</li> <li>DPNRM_DG</li> <li>RDSYSST</li> <li>DP_TOPOL</li> </ul>	7 Yes see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes Yes SFC 11; per interface 8; SFC 12; per interface 8; SFC 59; per interface 8; SFC 59; per interface 8; SFC 56; per interface 8; SFC 57; per interface 8; SFC 56; per interface 8; SFC 56; per interface 8; SFC 56; per interface 8; SFC 56; per interface 8; SFC 13; per interface 8; SFC 13; per interface 8; SFC 51 1; SFC 103; per interface
<ul> <li>Access to consistent data in process image</li> <li>System functions (SFC)</li> <li>System function blocks (SFB)</li> <li>Programming language <ul> <li>LAD</li> <li>FBD</li> <li>STL</li> <li>SCL</li> <li>CFC</li> <li>GRAPH</li> <li>HiGraph®</li> </ul> </li> <li>configuration / programming / number of simultaneously</li> <li>DPSYC_FR</li> <li>D_ACT_DP</li> <li>RD_REC</li> <li>WR_PARM</li> <li>PARM_MOD</li> <li>WR_DPARM</li> <li>DPNRM_DG</li> <li>RDSYSST</li> </ul>	7 Yes see instruction list see instruction list Yes Yes Yes Yes Yes Yes Yes Yes Yes SFC 11; per interface 8; SFC 12; per interface 8; SFC 59; per interface 8; SFC 59; per interface 8; SFC 56; per interface 8; SFC 57; per interface 8; SFC 56; per interface 8; SFC 56; per interface 8; SFC 56; per interface 8; SFC 56; per interface 8; SFC 13; per interface 8; SFC 13; per interface 8; SFC 51 1; SFC 103; per interface

- WRREC	8; SFB 53; per interface, but not more than 32 across all external interfaces
Know-how protection	
<ul> <li>User program protection/password protection</li> </ul>	Yes
Block encryption	Yes; With S7 block Privacy
Dimensions	
Width	25 mm
Height	290 mm
Depth	219 mm
Weights	
Weight, approx.	700 g

last modified:

9/7/2023 🖸